

TC94S

| Class | Subclass |
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ISSUE CLASSIFICATION

U.S. UTILITY Patent Application

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| O.I.P.E. | PATENT DATE |
| AK SCANNED <u>12/23</u> O.A. <u>Am</u> | |

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|--|------------|---------------------|----------|------------------------------------|----------------|
| 09/823602 | | 27 13 | 501 | 2185 2182 | Lee |
| Joseph Jeddaloh | | | | 2185 2116 | Chen |
| Serial presence detect driven memory clock control | | | | | |

PTO-20
12/99

[illegible]

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|---|--|-------------|-----------------------------------|--------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | ISSUE FEE | |
| | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | ISSUE BATCH NUMBER | |
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